



IFW

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application

Inventors: H. SADAKATA, et al. Art Unit: 2818
Application No.: 10/624,890
Filed: July 23, 2003
For: SEMICONDUCTOR MEMORY DEVICE AND TEST METHOD
THEREOF

REQUEST FOR CORRECTED FILING RECEIPT

Assistant Commissioner of Patents
Washington, D.C. 20231

Sir:

Please issue a Corrected Filing Receipt reflecting the title of the invention as set forth in the Declaration for Patent Application filed December 18, 2003, to read as follows: --SEMICONDUCTOR MEMORY DEVICE AND TEST METHOD THEREOF--.

A copy of the Filing Receipt with changes marked in red ink is attached.

Respectfully submitted,

James E. Ledbetter
Registration No. 28,732

Date: June 9, 2004

JEL/mat

ATTORNEY DOCKET NO. L8462.03107

STEVENS, DAVIS, MILLER & MOSHER, L.L.P.
1615 L Street, NW, Suite 850
P.O. Box 34387
Washington, DC 20043-4387
Telephone: (202) 785-0100
Facsimile: (202) 408-5200



UNITED STATES PATENT AND TRADEMARK OFFICE

L8462.03107
 UNITED STATES DEPARTMENT OF COMMERCE
 United States Patent and Trademark Office
 Address: COMMISSIONER FOR PATENTS
 P.O. Box 1450
 Alexandria, Virginia 22313-1450
 www.uspto.gov

APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/624,890	07/23/2003	2818	1492	L8462.03107	9	13	5

24257

STEVENS DAVIS MILLER & MOSHER, LLP
 1615 L STREET, NW
 SUITE 850
 WASHINGTON, DC 20036



CONFIRMATION NO. 6756

UPDATED FILING RECEIPT



OC000000012189472

Date Mailed: 03/25/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Hiroyuki Sadakata, Osaka, JAPAN;
 Naoki Kuroda, Kyoto, JAPAN;

Domestic Priority data as claimed by applicant

Foreign Applications

JAPAN 2002-227058 08/05/2002

If Required, Foreign Filing License Granted: 11/28/2003

Projected Publication Date: 07/01/2004

Non-Publication Request: No

Early Publication Request: No

Title

test method thereof
 Semiconductor memory device and ~~method for testing the same~~

Preliminary Class